EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	27	(375/377,257,220,285.ccls.) and (phone near2 line) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:30
L2	8	(375/377,257,220,285.ccls.) and (phone near2 line) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation and (interface adj1 standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:43
L3	27	(375/377,257,220,285.ccls.) and (phone near2 line) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation and (standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:43
L4	7	(375/377,257,220,285.ccls.) and (phone near2 line) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation and (first near5 standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:44
L5	34	(379/412,90.01,399.01.ccls.) and (phone near2 line) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:45
L6	34	(379/412,90.01,399.01.ccls.) and (phone near2 line$1) and (dc adj1 holding) and (differential adj1 signal$1) and power and isolation and standard$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:47

L7	6	(379/412,90.01,399.01.ccls.) and (phone near2 line$1) and ((dc adj1 holding) near4 program$5) and (differential adj1 signal$1) and power and isolation and standard$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:55
L8	6	(379/412,90.01,399.01.ccls.) and (phone near2 line$1) and ((dc adj1 holding) near4 program$5) and (differential adj1 signal$1) and power and isolat$3 and standard$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/18 21:56
S1	0	((powered adj1 side adj1 circuit$2) with isolation) and ((dc adj1 holding adj1 circuit) with mode$1) same (first near4 interface adj1 standard) same (Second near4 interface adj1 standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 16:57
S2	0	((powered adj1 side adj1 circuit$2)) and ((dc adj1 holding adj1 circuit) with mode$1) same (first near4 interface adj1 standard) same (Second near4 interface adj1 standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 16:57
S3	6	((powered adj1 side adj1 circuit$2)) and ((dc adj1 holding adj1 circuit)) same (first near4 interface adj1 standard) same (Second near4 interface adj1 standard)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 16:58
S4	12	(isolation adj1 capacitor$1) with (bidirectionally) with (differential adj1 signal$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 17:07
S5	7	S4 and (clock with (phone adj1 line adj1 side adj1 circuit$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 17:08

S6	7	S5 and (Second adj1 isolation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 17:09
S7	9	((first near2 differential adj1 signal) with isolation) and ((Second near2 differential adj1 signal) with isolation) and (bi$1direction$4 near4 transfer) and (regulatory adj1 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/13 17:29
S8	1	("5600715").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/13 18:23
S9	0	(hold$3 adj1 circuit) with (termination adj1 impedance$1) and ((termination adj1 impedance$1) with (loop near2 current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 08:48
S10	2	(hold$3 adj1 circuit) with (termination adj1 impedance$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 08:49
S11	4	(hold$3 adj1 circuit) with (termination near3 impedance$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 08:50
S12	12	(isolation adj1 capacitor$1) with (bidirectionally) with (differential adj1 signal$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 10:14
S13	4	(dc adj1 holding adj1 circuit) and (phone adj1 line adj1 side) and (powered adj1 side) and (digital adj1 differential adj1 signal) and (regulatory adj1 standard$1) and (bi$1directional$2 near2 transfer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 10:40

S14	4	(dc adj1 holding adj1 circuit) and (phone adj1 line adj1 side) and (powered adj1 side) and (first adj1 digital adj1 differential adj1 signal) and (regulatory adj1 standard$1) and (bi$1directional$2 near2 transfer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 10:40
S15	0	(dc adj1 holding adj1 circuit) and (phone adj1 line adj1 side) and (powered adj1 side) and (termination adj1 impedance$1) and (loop adj1 current adj1 level)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 10:47
S16	8	(dc adj1 holding adj1 circuit) and (phone adj1 line adj1 side) and (powered adj1 side) and (termination adj1 impedance$1) and (loop adj1 current)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 10:47
S17	0	(dc adj1 holding adj1 circuit) and (phone adj1 line adj1 side) and (powered adj1 side) and (termination adj1 impedances) and (loop adj1 current)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2008/02/14 10:48
S18	2	((hold$3 adj1 circuit) with (phone adj1 line)) and ((hold$3 adj1 circuit) with (termination adj1 impedance$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 11:12
S19	6	(first adj1 phone adj1 line adj1 interface) and (second adj1 phone adj1 line adj1 interface) and (dc adj1 holding adj1 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 13:32
S20	6	(first adj1 phone adj1 line adj1 interface) and (second adj1 phone adj1 line adj1 interface) and (dc adj1 holding adj1 circuit) and (differential adj1 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 13:34

S21	3	(first adj1 phone adj1 line adj1 interface) and (second adj1 phone adj1 line adj1 interface) and (dc adj1 holding adj1 circuit) and ((differential adj1 signal) same (isolation adj1 capacitor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 13:34
S23	6	(dc adj1 holding adj1 circuit) same (first near3 (interface adj1 standard))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 15:48
S24	1	("5500894").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/14 15:52
S25	0	((dc adj1 holding adj1 circuit) with (power near2 dissipat$3)) and (powered adj1 line adj1 side) and (phone adj1 line adj1 side)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 16:39
S26	18	((dc adj1 holding adj1 circuit) with (power near2 dissipat$3)) and (powered adj1 side) and (phone adj1 line adj1 side)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 16:39
S27	4	((dc adj1 holding adj1 circuit) with (power near2 dissipat$3)) and (powered adj1 side) and (phone adj1 line adj1 side) and (first near2 differential adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 16:40
S28	7	(first near2 differential adj1 signal) with (first adj1 isolation adj1 capacitor) with (second adj1 isolation adj1 capacitor) and ((powered adj1 side) with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/14 16:48
S29	39	(dc adj1 holding adj1 circuit$2) and (power$2 adj1 side adj1 circuit$2) and (phone adj1 line adj1 side) and ((differential adj1 signal) with isolation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:49

S30	39	S29 and (power$2 adj1 side) with clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:50
S31	0	("375"/$.ccls and "379"/$.ccls) and (dc adj1 hold$3) and (phone adj1 line adj1 side) and (power$2 adj1 side)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:54
S32	14	("375"/$.ccls. and "379"/$.ccls.) and (dc adj1 hold$3) and (phone adj1 line adj1 side) and (power$2 adj1 side)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:55
S33	14	S32 and (differential adj1 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:55
S34	11	S33 and (isolation adj1 capacitor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 07:55
S35	1	("5598288").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:45
S36	1	("20040028414").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:46
S37	1	("20040156673").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:46
S38	1	("6724890").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:46
S39	1	("6973183").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:47
S40	1	("20020030579").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:47

S41	1	"4292595".PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:48
S42	0	("4292595,4835486,4843339,4901275,5287107,").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 08:49
S43	0	"4292595,4835486,4843339,4901275,5287107",	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 08:49
S44	165	"4292595" "4835486" "4843339" "4901275" "5287107"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 08:50
S45	1079	"4292595" "4835486" "4843339" "4901275" "5287107" "5359666" "5500498" "5500895" "5602912" "4043720" "5428682" "4757528" "4192975" "4417099" "4529845" "4540854" "4567331" "4644526" "4741031" "4864605" "4993063" "4995111" "5097503" "5136630" "5204896" "5235634" "5245654" "5329585" "5410594" "5329281" "6555010" "5692041" "5069659" "5337338" "5654984" "5506900" "5790656" "5384808" 58/ 83907 "5974139" "5533053" "4607170" "4901217" "5032819" "5086454" "5224154" "5473552" "4943807" "4349821" "3981006" "5805687" "5390249" "4686697" "5355407" "5592529" "5555100" "5870046" "5459721" "4656661" "5991335" "4415777"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 08:56
S46	19	S45 and (phone adj1 line adj1 side) and (hold$3 adj1 circuit) and isolation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 08:58

S47	1661	(phone adj1 line adj1 side) and (hold$3 adj1 circuit) and (isolation) and "5051799" "5705978" "5740241" "6005923" "5901210" "5377260" "5343516" "5841840" "5546448" "5875235" "5737411" "5511118" "5721774" "5659608" "5258721" "5881129" "5426698" "4054941" "4712233" "5959372" "5333196" "5946393" "4594577" "4384274" "4237511" "3750024" "5917364" "4720811" "5355468" "5751803" "5544241" "5577104" "5675640" "5509068" "5442694" "5323451" "5818923" "5859584" "5848133" "5940459" "4657385" "5644629" "5907596" "5262779" "5361067" "5745060" "5598157" "6061051" "5343514" "4541104" "4066843" "4118603" "4393516" "4996529" "6072778" "4475208" "5561712" "5506903" "5694465" "5361296" "5260941" "5375051" "6111939" "5881102" "5914671" "3904833" "5406283" "5521967" "5311588" "5982317" "6081586" "4528664" "4686699" "5323398" "4224478" "5058129" "6205219" "6128373" "4465903" "6124756" "5600551"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 09:07
S48	1702	"5051799" "5705978" "5740241" "6005923" "5901210" "5377260" "5343516" "5841840" "5546448" "5875235" "5737411" "5511118" "5721774" "5659608" "5258721" "5881129" "5426698" "4054941" "4712233" "5959372" "5333196" "5946393" "4594577" "4384274" "4237511" "3750024" "5917364" "4720811" "5355468" "5751803" "5544241" "5577104" "5675640" "5509068" "5442694" "5323451" "5818923" "5859584" "5848133" "5940459" "4657385" "5644629" "5907596" "5262779" "5361067" "5745060" "5598157" "6061051" "5343514" "4541104" "4066843" "4118603" "4393516" "4996529" "6072778" "4475208" "5561712" "5506903" "5694465" "5361296" "5260941" "5375051" "6111939" "5881102" "5914671" "3904833" "5406283" "5521967" "5311588" "5982317" "6081586" "4528664" "4686699" "5323398" "4224478" "5058129" "6205219" "6128373" "4465903" "6124756" "5600551"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 09:09
S49	11	S48 and (phone adj1 line adj1 side) and (hold$3 adj1 circuit) and (isolation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 09:10
S50	1	("5500894").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 10:21
S51	7	((dc adj1 impedance) or (dc adj1 hold$3 adj1 circuit)) with standard with (current adj1 limit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:30

S52	7	(phone adj1 line adj1 interface adj1 standard) with ((dc adj1 current) near2 requirement$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:39
S53	7	((phone adj1 line) near2 standard) with ((dc adj1 current) near2 requirement$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:41
S54	7	((phone adj1 line) near2 standard) with ((dc adj1 current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:41
S55	6	((phone) near2 standard) with ((dc adj1 current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:41
S56	7	((phone) near4 standard) with ((dc adj1 current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:41
S57	8	((phone) near4 standard) with ((dc near2 current))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:45
S58	232	((telephonephone or phone) near4 standard$1) with ((current ajd1 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:46

S59	0	((telephonephone or phone) near4 standard$1) with ((current adj1 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:46
S60	1	((telephonephone or phone) near4 standard$1) with ((current near2 level))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 10:46
S61	9	((telephonephone or phone) near4 standard$1) with ((current near3 requirement$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 11:03
S62	1	("7046755").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 11:33
S63	1	("7046755").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/02/15 11:37
S64	16	((channel near3 estimat$3) with (combin$3 or add$3 or sum$3)) same (adaptive near3 equaliz$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/02/15 13:56
S65	0	(phone near2 line) and ((dc adj2 holding) near5 mode$1) and ((power $2 adj1 circuit) with clock) and ((differential near2 signal) with isolat $3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:14
S66	0	(phone near2 line) and ((dc adj2 holding) near5 mode$1) and ((power $2 near2 circuit) with clock) and ((differential near2 signal$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:15
S67	0	(phone near2 line) and ((dc adj2 holding) with mode$1) and ((power $2 near2 circuit) with clock) and ((differential near2 signal$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:15

S68	0	(phone near2 line) and ((dc adj2 holding) with mode$1) and ((power $2 near2 circuit) with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:15
S69	12	(phone near2 line) and ((dc adj2 holding) with mode$1) and ((power $2 near2 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:15
S70	10	(phone near2 line) and ((dc adj2 holding) with mode$1) and ((power $2 near2 circuit)) and differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:16
S71	68	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:17
S72	0	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and "ccls." 375/377,257,220,285	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:18
S73	0	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and "ccls." 375/377,257,220,285.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:18
S74	26	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and 375/377,257,220,285.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:20

S75	33	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and 379/412,90.01,399.01.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:24
S76	66	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and power and isolat$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:25
S77	12	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and (current$1limit$3 or (current adj1 limit$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:36
S78	60	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and clock and (isolat$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:38
S79	60	(phone near2 line) and ((dc adj2 holding)) and ((power$2 near2 circuit)) and clock and (isolat$3) and (power or energy)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:39
S80	60	S79 and (isolat$3 near3 capacitor$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:41
S81	48	S80 and (power with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/25 16:46

S82	0	((power$2 near3 circuit$3) near6 (differential adj1 signal) near4 (isolat$3 or capacitor$1)) and ((($4phone or line) near2 circuit) near6 (differential adj1 signal) near5 (isolat$3 or capacitor$1)) and (power$2 near7 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:41
S83	0	((power$2 near3 circuit$3) near8 (differential adj1 signal) near8 (isolat $3 or capacitor$1)) and ((($4phone or line) near2 circuit) near8 (differential adj1 signal) near8 (isolat$3 or capacitor$1)) and (power$2 near7 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:42
S84	1	((power$2 near3 circuit$3) with (differential adj1 signal) with (isolat$3 or capacitor$1)) and ((($4phone or line) near3 circuit) with (differential adj1 signal) with (isolat$3 or capacitor$1)) and (power$2 near7 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:44
S85	1	((power$2 near3 circuit$3) with (differential adj1 signal) with (isolat$3 or capacitor$1)) and ((($4phone or line) near3 circuit) with (differential adj1 signal) with (isolat$3 or capacitor$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:47
S86	849	(dc near2 holding)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:51
S87	90	(dc near2 holding) and ($4phone near3 line) and (power$2 near3 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:52
S88	18	((dc near2 holding) with (switch$3 or mode$1)) and ($4phone near3 line) and (power$2 near3 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 09:53

S89	38	(dc near2 holding) and ($4phone near3 line) and (power$2 near3 circuit$3) and (internal with dc) and (external with dc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 10:02
S90	78	(dc near2 holding) and ($4phone near3 line) and (power$2 near3 circuit$3) and ((more or less or dissipat$3) near6 (power or energy))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 10:09
S91	77	(dc near2 holding) and ($4phone near3 line) and (power$2 near3 circuit$3) and ((dissipat$3 or sav$3 or loos$3 or lost$3 or hold$3) near6 (power or energy))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/07/28 10:10
S92	0	(phone adj1 line adj1 side) and (powered adj1 side adj1 circuit) and (dc adj1 holding adj1 circuit) and (first adj1 differential adj1 signal) and (second adj1 differential adj1 signal) and (isolation adj1 regulatory adj1 standards) and (isolation adj1 capacitors)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:20
S93	0	(phone adj1 line adj1 side) and (powered adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 differential adj1 signal) and (second adj1 differential adj1 signal) and (regulatory adj1 standards) and (isolation adj1 capacitors)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:22
S94	0	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 differential adj1 signal) and (second adj1 differential adj1 signal) and (regulatory adj1 standards) and (isolation adj1 capacitors)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:23
S95	7	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 digital adj1 differential adj1 signal) and (second adj1 digital adj1 differential adj1 signal) and (regulatory adj1 standards) and (isolation adj1 capacitors)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:25

S96	7	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 digital adj1 differential adj1 signal) and (second adj1 digital adj1 differential adj1 signal) and (regulatory adj1 standards) and (isolation adj1 barrier) and (power dissipated)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:29
S97	37435	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 digital adj1 differential adj1 signal) and (second adj1 digital adj1 differential adj1 signal) and (isolation adj1 barrier) and (power dissipated) amd (clock adj1 signal) and second adj1 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:32
S98	3	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (first adj1 digital adj1 differential adj1 signal) and (second adj1 digital adj1 differential adj1 signal) and (isolation adj1 barrier) and (power dissipated) and (clock adj1 signal) and second adj1 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:33
S99	10	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (isolation adj1 barrier) and (current adj1 limiting adj1 mode) and (external) and internal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:34
S100	10	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (isolation adj1 barrier) and (current adj1 limiting adj1 mode) and (external) and internal and power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:34
S101	10	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (isolation adj1 barrier) and (current adj1 limiting adj1 mode) same (switch$3) and (external) and internal and power and (isolation adm1 capacitor$1) and standards	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:37
S102	10	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (isolation adj1 barrier) and (current adj1 limiting adj1 mode) same (switch$3) and (external) and internal and power and (isolation adm1 capacitor$1) and standards and (differential adj1 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:38

S103	0	(phone adj1 line adj1 side) same (power$2 adj1 side adj1 circuit$2) same (dc adj1 holding) same (isolation adj1 barrier) same (current adj1 limiting adj1 mode) same (switch$3) same (external) and internal same power same (isolation adm1 capacitor$1) same standards same (differential adj1 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:43
S104	27	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (power near2 dissipat$3) and (external) and internal and (differential adj1 signal$1) and (isolation adj1 capacitor$1) and (clock) and (standards)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:51
S105	6	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (power near2 dissipat$3) and (external) and internal and (differential adj1 signal$1) and (isolation adj1 capacitor$1) and (clock) and (regulatory adj1 standards) and bidirectionally and (isolation adj1 elements)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:53
S106	6	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (power near2 dissipat$3) and (external) and internal and (differential adj1 signal$1) and (isolation adj1 capacitor$1) and (clock) and (regulatory adj1 standards) and bidirectional$2 and (isolation adj1 elements)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 14:53
S107	27	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (power near2 dissipat$3) and (external) and internal and (differential adj1 signal$1) and (isolation adj1 capacitor$1) and (clock) and bidirectional$2 and (isolation adj1 elements)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 15:05
S108	27	(phone adj1 line adj1 side) and (power$2 adj1 side adj1 circuit$2) and (dc adj1 holding) and (power near2 dissipat$3) and (external) and internal and (differential adj1 signal$1) and (isolation adj1 capacitor$1) and (clock) and bidirectional$2 and (isolation adj1 elements) and standards	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 15:05
S116	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2) near7 clock) and (dc adj1 holding) and (integrated adj1 circuit$4) and (non$1integrated) and (external adj1 circuit$3) and (internal adj1 circuit$3) and ((first adj1 isolation adj1 capacitor) with (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) with Second adj1 differential adj1 signal) with (bidirectional$4)) and (isolation adj1 element$1) and (regulatory adj1 standards)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:27

S118	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2) near7 clock) and (dc adj1 holding) and (integrated adj1 circuit$4) and (non$1integrated) and (external adj1 circuit$3) and (internal adj1 circuit$3) and ((first adj1 isolation adj1 capacitor) with (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) with (Second adj1 differential adj1 signal) with (bidirectional$4)) and (isolation adj1 element$1) and (regulatory adj1 standards)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:28
S119	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2) near7 clock) and (dc adj1 holding) and (integrated adj1 circuit$4) and (non$1integrated) and (external adj1 circuit$3) and (internal adj1 circuit$3) and ((first adj1 isolation adj1 capacitor) same (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) same (Second adj1 differential adj1 signal) same (bidirectional$4)) and (regulatory adj2 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:28
S120	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2) near7 clock) and (dc adj1 holding) and (integrated) and (non $1integrated) and (external) and (internal) and ((first adj1 isolation adj1 capacitor) same (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) same (Second adj1 differential adj1 signal) same (bidirectional$4)) and (regulatory adj2 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:29
S121	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2) near7 clock) and (dc adj1 holding) and (integrated) and (non $1integrated) and (external) and (internal) and ((first adj1 isolation adj1 capacitor) and (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) and (Second adj1 differential adj1 signal)) and (regulatory adj2 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:30
S122	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (non$1integrated) and (external) and (internal) and ((first adj1 isolation adj1 capacitor) and (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) and (Second adj1 differential adj1 signal)) and (regulatory adj2 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:31
S123	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (external) and (internal) and ((first adj1 isolation adj1 capacitor) and (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) and (Second adj1 differential adj1 signal)) and (regulatory adj2 standard$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:32

S124	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (external) and (internal) and ((first adj1 isolation adj1 capacitor) and (second adj1 isolation adj1 capacitor)) and ((first adj1 differential adj1 signal) and (Second adj1 differential adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:32
S125	0	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (external) and (internal) and (first adj1 isolation adj1 capacitor) and ((first adj1 differential adj1 signal) and (Second adj1 differential adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:32
S126	5	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (external) and (internal) and (first adj1 isolation adj1 capacitor) and ((first adj2 differential adj1 signal) and (Second adj2 differential adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:33
S127	5	(phone adj1 line adj1 side) and ((power$2 adj1 side adj1 circuit$2)) and (dc adj1 holding) and (integrated) and (external) and (internal) and (first adj1 isolation adj1 capacitor) and ((first adj2 differential adj1 signal) and (Second adj2 differential adj1 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2008/12/11 16:33

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